

ECR#: U1

Tracker #: None

Status: **[Proposed]**

Title: microATX MB to I/O aperture, correct the dimension, and text.

Release Date: August 28, 1998

Impact, Low: I/O aperture

Spec. Version: microATX Motherboard Interface Specification V1.0

#### Summary:

The dimension between the bottom of the motherboard to bottom of the chassis I/O opening needs to be corrected in Figure 4. The hatched area description for Figure 5 needs to be corrected.

#### Changes Current Specification As Shown:

##### Section 2.3.4 Back Panel I/O

Replace Figure 4 with the new figure provided here. (0.088 – .010 was 0.086 – .010)

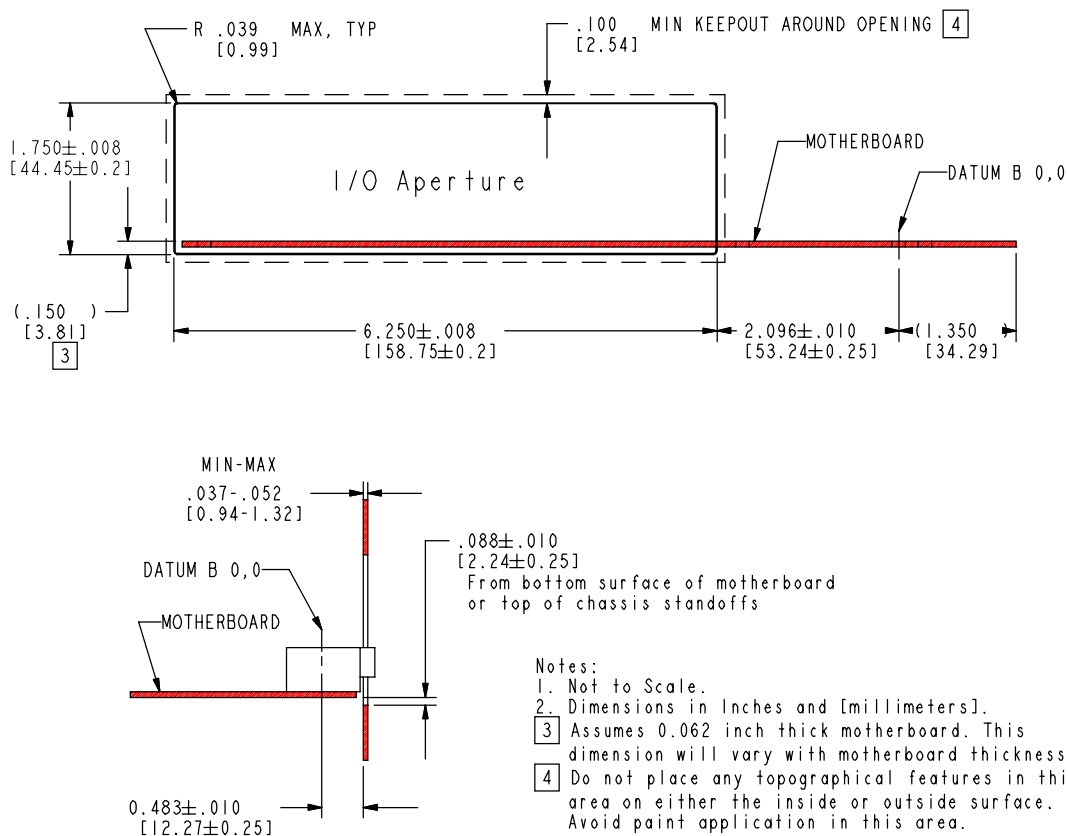


Figure 4: Chassis I/O Aperture Requirements

Replace the following text with the new text:

(hatched area in Figure 5: 8.216 inches [208.69mm] by 2.226 inches [56.54mm])

with:

(hatched area in Figure 5: 5.990 inches [152.15mm] by 1.470 inches [37.34mm])